

**ABSTRACT OF THE DISCLOSURE**

A multi-domain liquid crystal display device comprises first and second substrates facing each other; a liquid crystal layer between the first and second substrates; a plurality of gate bus lines arranged in a first direction on the first substrate and a plurality of data bus lines arranged in a second direction on the first substrate to define a plurality of pixel region; a thin film transistor positioned at a crossing area of the data bus line and the gate bus line, the thin film transistor comprising a gate electrode, a semiconductor layer, and source/drain electrodes; a common-auxiliary electrode comprising at least one electrode in the each pixel region; a plurality of pixel electrodes electrically charged through the thin film transistor; and an alignment layer on at least one substrate between the first and second substrates.